IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Megumi YOKOI et al.

Serial No. 09/645,880   Group Art Unit: 2186

Confirmation No. 4079

Filed: August 25, 2000   Examiner: Denise Tran

For: MULTIPROCESSOR SYSTEM AND MEMORY ACCESS METHOD

AMENDMENT

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Technology Center 2100

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed December 11, 2002, and having a period for response set to expire on March 11, 2003. A Petition for a three-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to June 11, 2003.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

IN THE CLAIMS:

Please REPLACE claims and ADD new claims, in accordance with the following:

1.   (AS ONCE AMENDED HEREIN) A memory access method for a multiprocessor system which includes a plurality of system modules coupled via a crossbar module, each of the system modules including a buffer which holds data and a plurality of processors having a cache memory which temporarily holds data, said memory access method comprising:

responsive to a read request from a processor within an arbitrary system module,

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